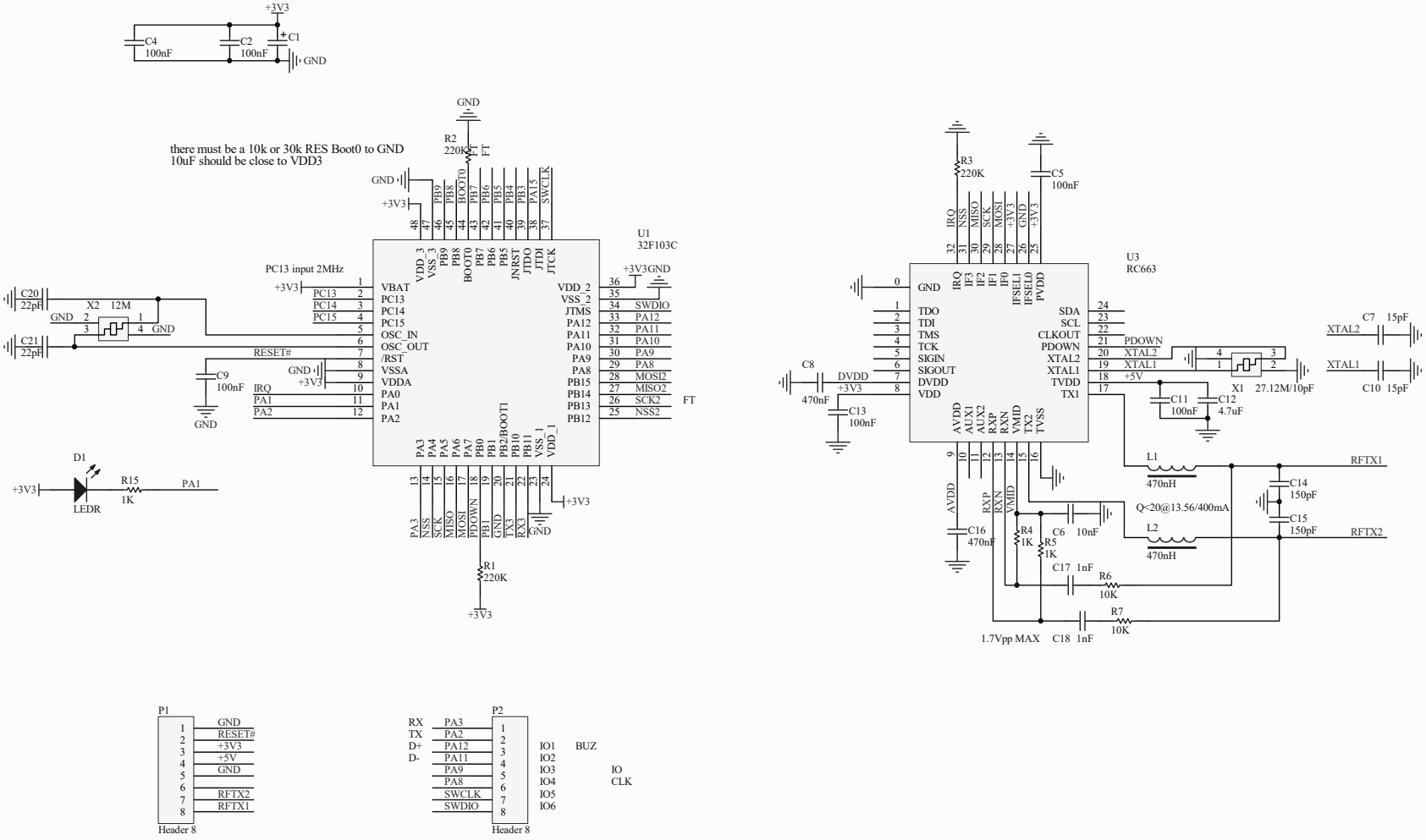
+3V3
C4
100nF
C2
100nF
C1
GND
there must be a 10k or 30k RES Boot0 to GND
10uF should be close to VDD3
R2
220K
FT
U1
32F103C
PC13 input 2MHz
C20
22pF
C21
22pF
X2 12M
RESET#
C9
100nF
IRQ
PA1
PA2
D1
LEDR
R15
1K
PA1
R1
220K
FT
R3
220K
C5
100nF
U3
RC663
C8
470nF
C13
100nF
C11
100nF
C12
4.7uF
X1 27.12M/10pF
C7 15pF
C10 15pF
XTAL2
XTAL1
L1
470nH
L2
470nH
C14
150pF
C15
150pF
RFTX1
RFTX2
Q<20@13.56/400mA
C16
470nF
C6 10nF
R4
1K
R5
1K
C17 1nF
R6
10K
R7
10K
1.7Vpp MAX
C18 1nF
P1
GND
RESET#
+3V3
+5V
GND
RFTX2
RFTX1
Header 8
P2
RX PA3
TX PA2
D+ PA12
D- PA11
PA9
PA8
SWCLK
SWDIO
IO1 BUZ
IO2
IO3
IO4
IO
IO5
CLK
IO6
Header 8